# Informatics 1 <br> Lecture 2: Abstract machines 

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(1) Turing machine
(2) RAM-machine (random access machine)

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- $F \subseteq Q$ the set of "final states" (this is when the machine stops),
- $\delta:(Q \backslash F) \times \Gamma \hookrightarrow Q \times \Gamma \times\{L, R\}$ is a partial function called the "transition function", where L is left shift, R is right shift (moves the tape)


## Turing completeness

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- A computational/data manipulation system is Turing complete if it can implement any Turing machine.
- Busy beaver (Tibor Radó, 1962) The Turing machine that writes the most non-empty symbols on an empty tape, and halts in finite steps.


|  | A | 000000000000 |
| :---: | :---: | :---: |
| 2 | B | 000000100000 |
| 3 | A | 000011000000 |
| 4 | C | 0001101000000 |
| 5 | B | 0011101000000 |
| 6 | A | 0111101000000 |
| 7 | B | 00111\|1100000 |
| 8 | B | 00011\|1110000 |
| 9 | B | 0000111111000 |
| 10 | B | 0000011111100 |
| 11 | B | $00000\|0\| 111110$ |
| 12 | A | 00001\|1111100 |
| 13 | C | 00011\|1111000 |
|  | H | 00011\|1111000 |

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|  |  | 000000000000 |
| :---: | :---: | :---: |
| 2 | B | 000000100000 |
| 3 | A | 0000111000000 |
| 4 | C | 0001100000000 |
| 5 | B | 0011101000000 |
| 6 | A | 0111101000000 |
| 7 | B | 001111100000 |
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| 9 | B | 0000111111000 |
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- $\Gamma=\{0,1\}$


|  | A | 0000010000000 |
| :---: | :---: | :---: |
| 2 | B | 0000010100000 |
| 3 | A | 00001\|1000000 |
| 4 | C | 0001100000000 |
| 5 | B | 0011100000000 |
| 6 | A | 0111100000000 |
| 7 | B | 001111100000 |
| 8 | B | 00011\|1110000 |
| 9 | B | 00001\|1111000 |
| 10 | B | 00000\|1111100 |
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- $b=0$ (empty symbol)


| 1 | A | 000000 | 0000 |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 2 | B | $00000 \mid 0$ | 11000 | 0 |  |
| 3 | A | 000011 | 10000 |  |  |
| 4 | C | 000110 | 10000 |  |  |
| 5 | B | 001110 | 10000 |  |  |
| 6 | A | 011110 | 0000 |  |  |
| 7 | B | 001111 | 11000 |  |  |
| 8 | B | 000111 | 1100 |  |  |
| 9 | B | 000011 | 1110 |  |  |
| 10 | B | 000001 | 1111 |  |  |
| 11 | B | 000000 | \|1111 |  |  |
| 12 | A | 0000111 | 1111 |  |  |
| 13 | C | 000111 | 1110 | 0 |  |
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|  | A | $00000\|0\| 000000$ |
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| 2 | B | 00000\|0100000 |
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- $F=\{$ HALT $\}$
- $\delta$ table:

|  | A | B | C |
| :---: | :---: | :---: | :---: |
| 0 | 1 RB | 1 LA | 1 LB |
| 1 | 1 LC | 1 RB | 1 RH |




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- explicit: the operand $n$ is a number (denoted by an $=$ at the end of the expression)
- direct: the operand $n$ is a memory cell, the operation is done with the contents of $r[n]$,
- indirect: the operand $n$ is the index of a memory cell, the operation is done with $r[r[n]]$ (denoted by a * at the end of the expression)


## Controller commands

JUMP $n$ jump to the $n$th command
JZERO $n$ jump to the $n$th command if $r_{0}=0$
JGTZ $n$ jump to the $n$th command if $r_{0}>0$
HALT stop
Arithmetic commands

## direct

ADD $n \quad r_{0} \leftarrow r_{0}+r_{n} \quad$ ADD $* \quad n \quad r_{0} \leftarrow r_{0}+r_{r_{n}}$ SUB $n \quad r_{0} \leftarrow r_{0}-r_{n} \quad$ SUB $* \quad n \quad r_{0} \leftarrow r_{0}-r_{r_{n}}$ MULT $n \quad r_{0} \leftarrow r_{0} * r_{n} \quad$ MULT $* n \quad r_{0} \leftarrow r_{0} * r_{r_{n}}$ DIV $n \quad r_{0} \leftarrow r_{0} / r_{n} \quad$ DIV* $n \quad r_{0} \leftarrow r_{0} / r_{r_{n}}$
direct
LOAD $n \quad r_{0} \leftarrow r_{n}$ STORE $n \quad r_{n} \leftarrow r_{0}$
indirect
explicit op

READ $n$ reads $n$ numbers from the input into $r_{1}, r_{2}, \ldots, r_{n}$ WRITE $n$ writes $n$ numbers to the output from $r_{1}, r_{2}, \ldots, r_{n}$

Write a program to calculate ( $a, b$ ) (greatest common divisor), where $a, b \in \mathbb{N}_{0}$ !

| p | command | operand | notes |
| :---: | :---: | :---: | :---: |
| 0 | LOAD = | 12 |  |
| 1 | STORE | 1 | $\mathrm{r}[1]$ - a |
| 2 | LOAD = | 16 |  |
| 3 | STORE | 2 | $\mathrm{r}[2]<-\mathrm{b}$ |
| 4 | JZERD | 17 |  |
| 5 | LOAD | 1 | $\mathrm{r}[0]<-\mathrm{r}[1]$ |
| 6 | DIV | 2 | $r[0]<-\lfloor a / b\rfloor$ |
| 7 | STORE | 3 | $r[3]<-\lfloor a / b\rfloor$ |
| 8 | MULT | 2 |  |
| 9 | STORE | 4 | $\mathrm{r}[4]<-\mathrm{b} *\lfloor\mathrm{a} / \mathrm{b}\rfloor$ |
| 10 | LOAD | 1 |  |
| 11 | SUB | 4 | $\mathrm{r}[0]<-\mathrm{a}-\mathrm{b} *\lfloor\mathrm{a} / \mathrm{b}\rfloor=\mathrm{a} \bmod \mathrm{b}$ |
| 12 | STORE | 5 |  |
| 13 | LOAD | 2 |  |
| 14 | STORE | 1 | $\mathrm{r}[1]<-\mathrm{b}$ |
| 15 | LOAD | 5 | b <- $\mathrm{a} \bmod \mathrm{b}$ |
| 16 | JUMP | 3 |  |
| 17 | LOAD | 1 |  |
| 18 | STORE | 6 | this is ( $\mathrm{a}, \mathrm{b}$ ) |
| 19 | HALT | 0 | 口 |

## What about real machines?

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- Registers are special CPU memories with extremely fast read-write speeds, but very limited capacity.


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(1) What is the machine code, assembly and the assembler?
(3) What are the contents of the memory after issuing these commands?

| 1 | LOAD $=$ | 5 |
| :--- | :--- | :--- |
| 2 | STORE | 1 |
| 3 | STORE $*$ | 1 |
| 4 | JZERO | 7 |
| 5 | LOAD $=$ | 2 |
| 6 | MUL | 1 |
| 7 | HALT |  |

